

Exam Init	Ref Des	Document No.	Date	Name	Class	Sub Class	FILING DATE If Appropriate
AJB		5,808,492	09/15/98	Chow	327	108	
CGB		5,974,241	10/26/99	Fusco	395	500.03	

1-825 U.S. PTO
08/692297

		Country & Doc. No. (11)	Pub. Date (43)		Class	Sub Class	Translation Yes No
Q8		EP 0 700 008	03/06/96	Peters et al.	G06F	17/50	

			Standard Search Report from patent application No. GBA 9925655 filed October 29, 1999
CSB			<u>IEEE Std 1164-1993: IEEE Standard Jmultivaluc Logic System for VHDL Model Interoperability (Std logic 1164).</u> 1993, IEEE, New York XP002135132, page 17

EXAMINER <i>Cynthia Barnes</i>	DATE CONSIDERED <i>09 Sept 2004</i>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.